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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,606	06/23/2003	Nobuhiro Jiwari	740819-1018	9586
22204 75	590 07/28/2004	EXAMINER		INER
NIXON PEABODY, LLP 401 9TH STREET, NW SUITE 900 WASHINGTON, DC 20004-2128			DEO, DUY VU NGUYEN	
			ART UNIT	PAPER NUMBER
			1765	

DATE MAILED: 07/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Asticus O	10/600,606	JIWARI ET AL.				
Office Action Summary	Examiner	Art Unit				
	DuyVu n Deo	1765				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	③(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 23 Ju	ne 2003.					
2a) This action is <b>FINAL</b> . 2b) ⊠ This	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	3 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-10 is/are pending in the application.	4) Claim(s) <u>1-10</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-10</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examiner	•					
10)⊠ The drawing(s) filed on 23 June 2004 is/are: a)	oxtimes accepted or b) $oxtimes$ objected to	by the Examiner.				
Applicant may not request that any objection to the o	lrawing(s) be held in abeyance. See	: 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction						
11) The oath or declaration is objected to by the Exa	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
a) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	have been received. have been received in Application ty documents have been receive (PCT Rule 17.2(a)).	on No. <u>09/708,086</u> . ed in this National Stage				
Attachment(s)	🗖					
Notice of References Cited (PTO-892)     Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) ∐ Interview Summary ( Paper No(s)/Mail Da					
Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 7/24/03		atent Application (PTO-152)				

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Xi et al. (US 6,211,065).

Xi describes a deposition method of amorphous fluorocarbon (claimed fluorine-containing organic film) having a low dielectric constant as slow as 2.3 using a fluorine source gas, such as CF4, C2F6, C3F8, C6F6, as the main component (col. 2, line 4-6; col. 12, line 34-44). Unlike claimed invention, he doesn't describe using fluorine gas such as C5F8 or C4F6. however, he teaches that other fluorine source can be used as long as the atomic ratios of F:C available in the chamber remain less than 2 (col. 10, line 58-64). It would have been obvious at the time of the invention for one skill in the art to use other fluorine gas such as claimed C5F8 or C4F6 as long as the atomic ratios of F:C available in the chamber remain less than 2 to deposit the organic film with an anticipation of an expected result.

3. Claims 2-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wang et al. (US 6,057,239), Xi et al. (US 6,211,065), and Imai et al. (US 6,057,247).

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Wang describes a method for forming a semiconductor device comprising: dry-etching an oxide film (claimed insulating film); depositing a low dielectric organic film (col. 3, line 40-60). Unlike claimed invention, Wang doesn't describe the etching gas containing C5F8, C3F6, or C4F6 as a main component. However, using gas such as C5F8, C4F6, or C3F6 as the main component for etching oxide film has been known to one skill in the art as taught by Imai (col. 15, line 6-7; col. 20, line 29-32). Therefore at the time of the invention, one skill in the art would find it obvious to etch the insulating film in light of Imai because Imai further describes techniques to etch the insulating film used by Wang in order to etch the insulating film.

Unlike claimed invention, Wang doesn't describe depositing a fluorine-containing organic film. However, he describes using low dielectric film and amorphous fluorocarbon is a low dielectric organic film as taught by Xi. Xi describes a deposition method of amorphous fluorocarbon (claimed fluorine-containing organic film) having a low dielectric constant as slow as 2.3 using a material gas including C3F6 as the main component (col. 2, line 4-6; col. 12, line 34-44). It would have been obvious at the time of the invention for one skill in the art to use an amorphous fluorocarbon film because it is a low dielectric organic film, which is used by Wang in order to form a sacrificial layer with an anticipation of an expected result. Even though, Xi doesn't describe using fluorine gas such as C5F8 or C4F6. However, he teaches that other fluorine source can be used as long as the atomic ratios of F:C available in the chamber remain less than 2 (col. 10, line 58-64). It would have been obvious at the time of the invention for one skill in the art to use other fluorine gas such as claimed C5F8 or C4F6 as long as the atomic ratios of F:C available in the chamber remain less than 2 to deposit the organic film with an anticipation of an expected result.

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According to the combined method above, the fluorocarbon gas is used in both, etching and depositing processes, on a same semiconductor substrate; therefore, it would have been obvious to do the etching and depositing in a same chamber of an apparatus in order to save processing time, and reduce contamination of substrate when it is moved from one chamber to another.

Referring to claim 4, according the method above, after the step of depositing the fluorocarbon film, the method (Wang's method) further comprises: forming a resist pattern on the insulating film, forming a wiring groove on the insulating film by dry etching using the resist as a mask; removing the resist and the fluorocarbon in the contact hole; and filling the contact hole and the wiring groove with a metal film to form a contact and a metal interconnection (col. 3, line 60-col. 4, line 21).

4. Claims 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsubara et al. (US 6,104,092).

Matsubara describes a method for forming a semiconductor substrate comprising: forming a metal film on the substrate; dry-etching the metal film using a resist pattern; depositing an amorphous carbon fluoride film (claimed fluorine-containing organic film) having a dielectric constant less than 4 (col. 2, line 54; col. 9, line 1-32). Unlike claimed invention, Matsubara doesn't describe depositing using C5F8, or C4F6 as a main component gas. He describes using other fluorocarbon gas including CF4, C4F8, C2F6, C3F8 (col. 9, line 60-68). However, It would have been obvious at the time of the invention for one skill in the art to use

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other fluorine gas such as claimed C5F8 or C4F6 in order to deposit the organic film with an anticipation of an expected result.

5. Claims 7, 8, and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsubara as applied to claim 6 above, and further in view of Frank (US 5,277,750).

Unlike claimed invention, Matsubara doesn't describe forming an insulating layer on the metal film and patterning the insulating film using a resist pattern. Frank teaches a method of patterning metal line. He teaches forming an insulating layer such as silicon dioxide on the metal layer and patterning the silicon oxide using a resist pattern (col. 6, line 42, line 58-61). It would have been obvious for one skill in the art to modify Matsubara in light of Frank because Frank describes that the silicon dioxide can be used a hard mask in order to etch the metal.

Unlike claimed invention, above prior art doesn't describe depositing the fluorocarbon film and etching the silicon dioxide and metal films in a same chamber. However, it would have been obvious to do the etching and depositing in a same chamber of an apparatus in order to save processing time, and reduce contamination of substrate when it is moved from one chamber to another.

6. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsubara and Frank as applied to claim 8 above, and further in view of Qian et al. (US 6,136,211).

Unlike claimed invention, above prior art doesn't describe the wall of the reactor chamber includes an aluminum layer and a ceramic layer or an alumite-treated aluminum layer.

Qian teaches an apparatus that can etch metal and insulating layer such as silicon dioxide. The

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chamber wall is fabricated from a variety of material including metals (aluminum oxide), ceramics, and composite materials (claimed aluminum and ceramic layer) (col. 5, line 31-46; col. 7, line 11-60). It would have been obvious for one skill in the art to use any apparatus that are available to one skill in the art such as one described by Qian in order to process the semiconductor with an anticipation of an expected result.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to DuyVu n Dec whose telephone number is 703-305-0515.

DVD July 26, 2004

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